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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10019885	FILING DATE 05/13/2002	CLASS 375	SUBCLASS 356	GAU 2631	EXAMINER PHAM CORRIEUS
**APPLICANTS: Balb Markus; Schedel Ralf; Fiessinger Walter;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/DE00/01735 05/29/2000					
** FOREIGN APPLICATIONS VERIFIED: GERMANY 199 30 191.3 06/30/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				8074-6 (S 1638 SB/fis)	
TITLE : Circuit for generating clock pulses in a communications system					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheeto Drwg.	Figso Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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